

## **In the Specification**

At page 1, after the title insert:

### **CROSS REFERENCE TO RELATED APPLICATION**

This patent application is a Continuation Application of U.S. Patent Application Serial No. 09/941,369 filed August 28, 2001, entitled "A Four Terminal Memory Cell, a Two-Transistor SRAM Cell, a SRAM Array, a Computer System, a Process for Forming a SRAM Cell, a Process for Turning a SRAM Cell OFF, a Process for Writing a SRAM Cell and a Process for Reading Data from a SRAM Cell", naming Leonard Forbes as inventor, the disclosure of which is incorporated herein by reference.